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SEP 12 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. MIYAMOTO, et al.
Application No.: 10/781,746
Filed: February 20, 2004
For: SEMICONDUCTOR INTEGRATED-CIRCUIT DEVICE AND
METHOD TO SPEED-UP CMOS CIRCUIT
Art Unit: 2819
Examiner: Vibol TAN

AMENDMENT

September 12, 2005

MS: AMENDMENTS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 12, 2005, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Drawings describing changes to Drawings shown in the appendix;

Amendments to the Claims;

Remarks are included following the amendments; and

Appendix included an amended Drawing Figure is attached following the

Remarks

09/13/2005 MBINAS 00000034 10781746

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